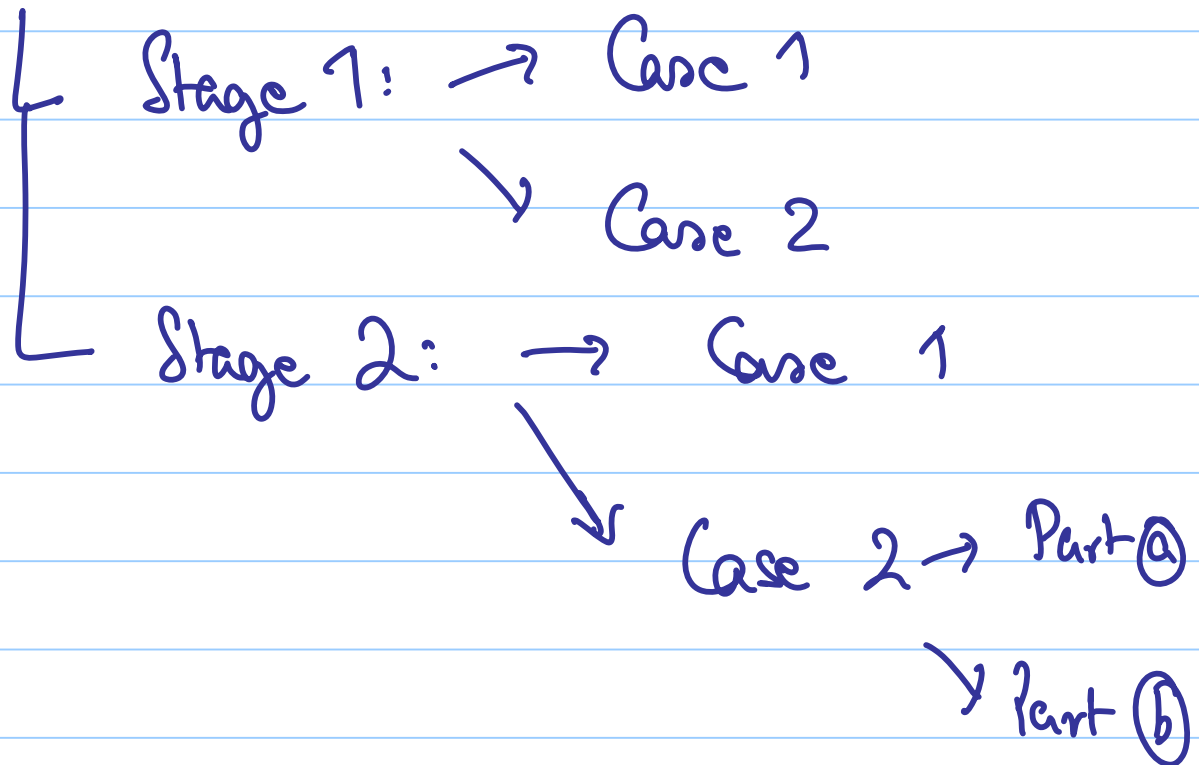


# LECT 05

Note Title

## TODAY

- Bounded depth circuits:  $AC^0$
- Parity  $\notin AC^0$  (mod "switching lemma")
- Proof of Switching Lemma.



AC<sup>0</sup>: (more generally AC<sup>k</sup>)

AC<sup>k</sup>: consist of poly-size circuits

of depth  $O(\log n)^k$

on  $\{ \infty\text{-AND}, \infty\text{-OR}, \text{NOT} \}$

gates



unbounded fanin.

[Not to be confused with NC<sup>k</sup>

NC<sup>k</sup>: poly size circuits

depth  $O(\log n)^k$

on  $\{ 2\text{-AND}, 2\text{-OR}, \text{NOT} \}$



binary gates.

NC<sup>0</sup> ⊆ AC<sup>0</sup> ⊆ NC<sup>1</sup> ⊆ AC<sup>1</sup> ⊆ . . . . .

Theorem: [ Furst, Saxe, Sipser ;  
Ajtai ;  
Yao ;  
Håstad ;  
Razborov ;  
Razborov-Smolensky ]

Parity  $\notin$  AC<sup>0</sup>

where Parity  $(x_1, \dots, x_n) = \sum_{i=1}^n x_i \pmod{2}$

## Key Ingredient: Switching Lemma

Informally: Random assignment to most variables in DNF formula leaves very simple function on remaining

[Will elaborate later on random, simple remaining]

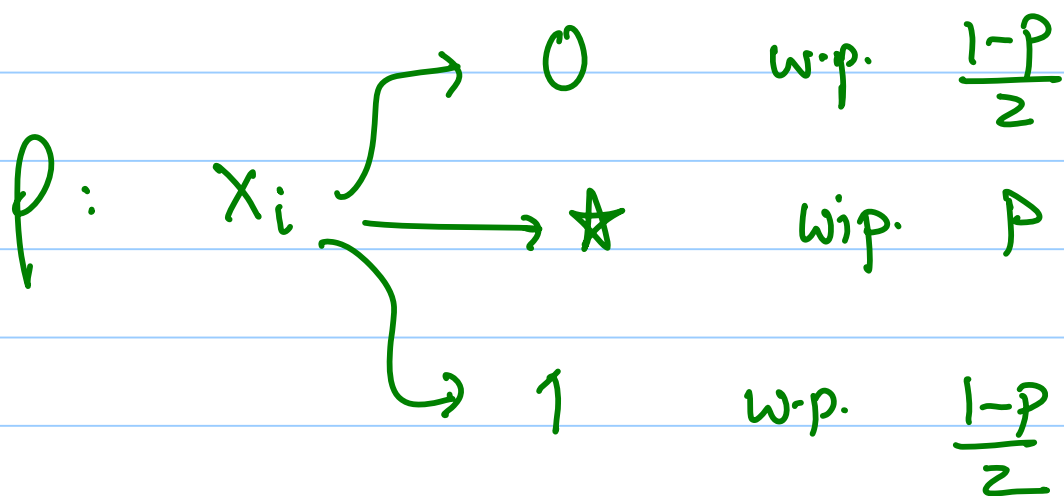
[Mod Switching Lemma]:

- Set most variables of depth  $d$  circuit randomly
- Bottom 2 levels become so simple that circuit becomes depth  $d-1$ .

- Function (parity) on remaining variables remains complex (still a parity)
- Induction implies new circuit can't compute parity.



Definition: Random restriction  $\rho$  of  $x_1, \dots, x_n$  with parameter  $p$  does the following



BASIC SWITCHING LEMMA:  $\forall c_1 < \infty \exists C < \infty$  st.

- let  $\rho$  be random restriction w-parameter  $p = \frac{1}{\sqrt{n}}$  ;

- let  $F$  be DNF formula of size  $S \leq n^{c_1}$

- Then  $\Pr [F|_{\rho} \text{ depends on } > C \text{ variables}] \leq \left(\frac{1}{n^{c_1}}\right)^2$

Notes: This version due to [F.S.S];

Improved versions due to [Håstad],  
[Razborov].

See survey by [Beame].

# Proof that Parity $\notin AC^0$ :

(assuming Basic S.L.)

- By induction assume  $\forall c < \infty$  s.t.  
no depth  $d$  circuit of size  $2^{n^c}$   
computes parity.

- Now suppose  $n^c$  size depth  $d+1$   
circuit  $G$  computes parity.

- Apply r.r.  $P$  with parameter  $p = \frac{1}{\sqrt{n}}$

-  $\downarrow (1 - 2^{-\sqrt{n}})$   
w.h.p.  $\sqrt{n}$  variables remain ;

-  $G|_P$  computes parity of  $\sqrt{n}$  variables ;

[S.O.L.]  
↓  
-

w.p.  $1 - \frac{1}{n^{2c}}$  any fixed gate at  
depth 2, computes fn. of  $C$  variables

- w.p.  $1 - \frac{1}{n^c}$  all gates compute

function of  $C$  variables

-  $\Rightarrow G|_p \approx \tilde{G}$  of size  $2^c \cdot n^c$   
& depth  $d$

Computing parity of  $\sqrt{n}$  variables

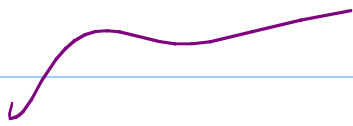
- Contradiction to induction if

$$2c < c$$





[Warning: Notes below somewhat  
incoherent / inconsistent]



# Proof of Switching Lemma

- No restriction in two stages

Stage 1:  $x_i$  remains w.p.  $\sqrt{p}$

Stage 2:  $x_i$  remains w.p.  $\sqrt{p}$

Claims:

Stage 1:  $\exists C$  depends only on  $\log_n S$

s.t. each term has only  $C$  variables;

Stage 2:  $\exists C$  depending on  $C$   
and  $\log_n S$

s.t. DNF depends on only  $C$  variables.

[all w.p.  $\geq 1 - \frac{1}{S^2}$ ]

Proof of Stage 1 claim: (relatively easy)

Case 1:  Fanout  $\geq 4 \log s$

$$\Pr \left[ \text{gate} \neq 0 \right] \leq \left( \frac{1}{3} \right)^{4 \log s}$$

$$\approx \frac{1}{s^3}$$

$$\Pr \left[ \exists \text{ gate with large fanout} \neq 0 \right] \leq \frac{1}{s^2}$$

Case 2:  Fanout  $\leq 4 \log s$

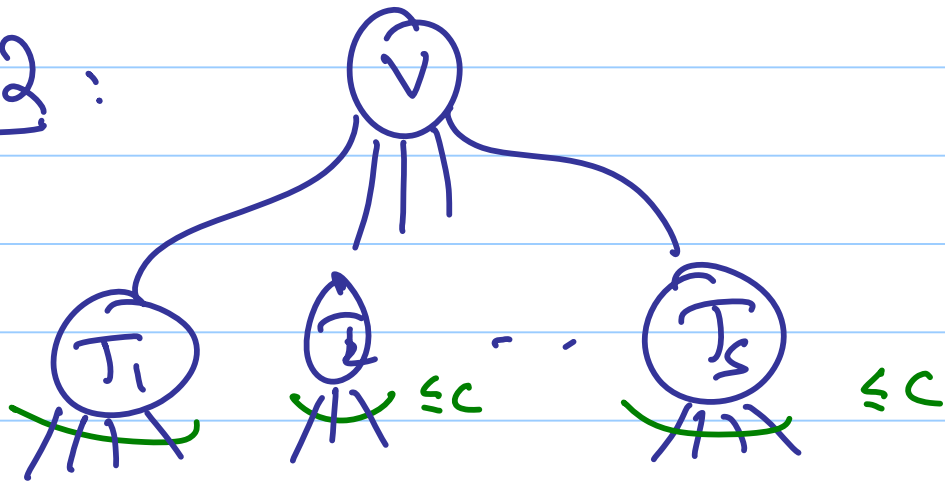
$$\Pr \left[ i \text{ variables unrestricted} \right] \leq$$

$$(4 \log s)^i (\sqrt{p})^i$$

$$\Pr \left[ \exists \text{ gate with } \geq c \text{ unset variables} \right] \leq s \cdot (4 \log s \cdot \sqrt{p})^c \dots$$

$$\leq \frac{1}{s^2} \quad \left[ \text{provided } c = \Omega(\log_n s) \right]$$

Stage 2:



Case 1: # disjoint  $T_i$ 's large ( $\Omega(\log s)$ )

$$\Pr [T_i = 1] \geq \left(\frac{1}{3}\right)^c$$

$$\Pr [\exists T_i = 1] \geq 1 - \left(1 - \left(\frac{1}{3}\right)^c\right)^{\log s}$$

$$\geq 1 - \frac{1}{s^2}$$

(in this case we are done)

Case 2: # disjoint  $T_i$ 's  $\leq 4 \log s$

Claim:  $\exists$   $4c \log s$  variables  $H$  that  
hit every  $T_i$

Proof: Select disjoint  $T_i$ 's greedily.

When stop variables in selected  $T_i$   
hit all  $T_i$ 's.  $\square$

Part 2: First restrict variables of  $H$ ;

As in Case 2 of Stage 1,

# unset variables  $\leq b'$ .

Part 2(b): - Now restrict variables not in  $H$ ;

& use induction on  $c$ .

- let  $C'$  be s.t. w.h.p.  $(c-1)$  DNF  
under random restriction depends on only  
 $C'$  variables

- Claim: w.h.p.  $c$ -DNF only depends on

$$C \leq b + 2^b \cdot C' \text{ variables.}$$

- Proof: - after restricting variables in  $H$ , enumerate

each assignment to unset variables of  $H$ ;

- each leads to  $(c-1)$ -DNF  $\Rightarrow$  depends on  
 $C'$  variables

- Thus this depends on  $\leq b + 2^b \cdot C'$  variables.  $\square$